

# RJK4532DPD

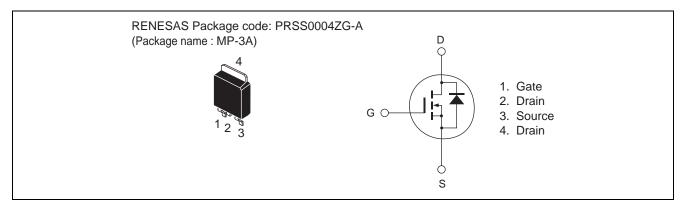
450V - 4A - MOS FET High Speed Power Switching R07DS0682EJ0100 Rev.1.00 Feb 24, 2012

Datasheet

#### Features

- Low on-state resistance
- $R_{DS(on)} = 1.9 \ \Omega$  typ. (at  $I_D = 2.0 \ A$ ,  $V_{GS} = 10 \ V$ ,  $Ta = 25^{\circ}C$ )
- Low drive current
- High speed switching

#### Outline



## **Absolute Maximum Ratings**

			$(Ta = 25^{\circ}C)$
Item	Symbol	Value	Unit
Drain to source voltage	V <sub>DSS</sub>	450	V
Gate to source voltage	V <sub>GSS</sub>	±30	V
Drain current	ID	4	A
Drain peak current	ID (pulse) Note1	16	A
Body-drain diode reverse drain current	I <sub>DR</sub>	4	A
Body-drain diode reverse drain peak current	Note1 I <sub>DR (pulse)</sub>	16	A
Avalanche current	AP Note3	3	A
Avalanche energy	E <sub>AR</sub> <sup>Note3</sup>	0.5	mJ
Channel dissipation	Pch Note 2	40.3	W
Channel to case thermal Impedance	θch-c	3.1	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. Pulse width limited by safe operating area.

2. Value at  $Tc = 25^{\circ}C$ 

3. STch = 25°C, Tch  $\leq$  150°C



## **Electrical Characteristics**

						$(Ta = 25^{\circ}C)$
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	V <sub>(BR)DSS</sub>	450	—	_	V	$I_{D} = 1 \text{ mA}, V_{GS} = 0$
Zero gate voltage drain current	I <sub>DSS</sub>	—	—	1	μA	$V_{DS} = 450 \text{ V}, V_{GS} = 0$
Gate to source leak current	I <sub>GSS</sub>	—	—	±0.1	μΑ	$V_{GS}=\pm 30~V,~V_{DS}=0$
Gate to source cutoff voltage	V <sub>GS(off)</sub>	3.5	—	4.5	V	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$
Static drain to source on state resistance	R <sub>DS(on)</sub>	—	1.9	2.3	Ω	$I_D = 2 \text{ A}, V_{GS} = 10 \text{ V}^{Note 4}$
Input capacitance	Ciss	—	280	_	pF	V <sub>DS</sub> = 25 V
Output capacitance	Coss	—	36	_	pF	V <sub>GS</sub> = 0 f = 1 MHz
Reverse transfer capacitance	Crss	—	4	_	pF	
Turn-on delay time	t <sub>d(on)</sub>	_	9.0		ns	I <sub>D</sub> = 2 A
Rise time	tr	—	4.5		ns	$V_{GS} = 10 V$ $R_L = 113 \Omega$ $Rg = 10 \Omega$
Turn-off delay time	$t_{d(off)}$	—	20.0	_	ns	
Fall time	t <sub>f</sub>	—	5.0	_	ns	
Total gate charge	Qg	—	9.0	_	nC	V <sub>DD</sub> = 360 V
Gate to source charge	Qgs	—	2.0	_	nC	$V_{GS} = 10 V$ $I_D = 4 A$
Gate to drain charge	Qgd	—	4.5	_	nC	
Body-drain diode forward voltage	V <sub>DF</sub>	—	0.9	1.5	V	$I_F = 4 \text{ A}, V_{GS} = 0^{\text{Note 4}}$
Body-drain diode reverse recovery time	t <sub>rr</sub>	—	215		ns	$I_F = 4 \text{ A}, V_{GS} = 0$
						di <sub>F</sub> /dt = 100 A/µs

Note: 4. Pulse test

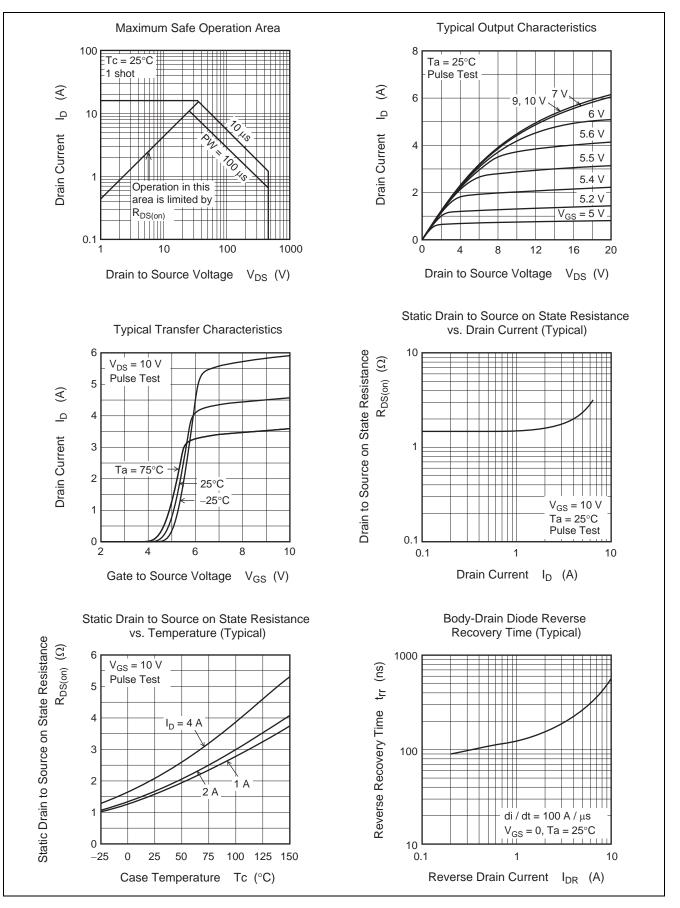
5. Since this device is equipped with high voltage FET chip ( $V_{DSS} \ge 600 \text{ V}$ ), high voltage may be supplied. Therefore, please be sure to confirm about Electric discharge between Drain terminal and other terminal.

6. This device is sensitive to electrostatic discharge.

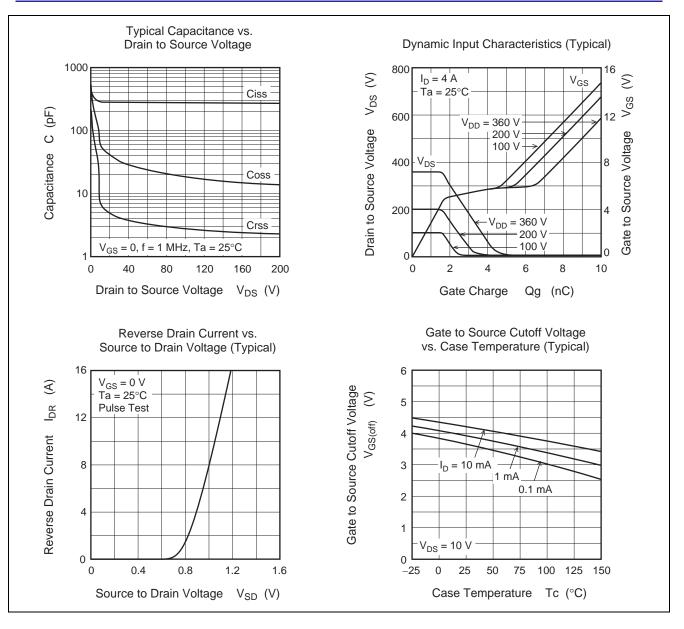
It is recommended to adopt appropriate cautions when handling this product.



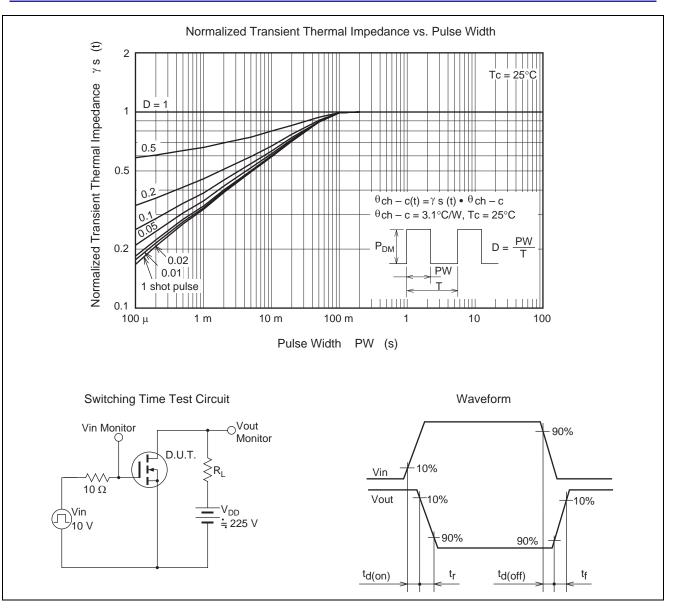
#### **Main Characteristics**





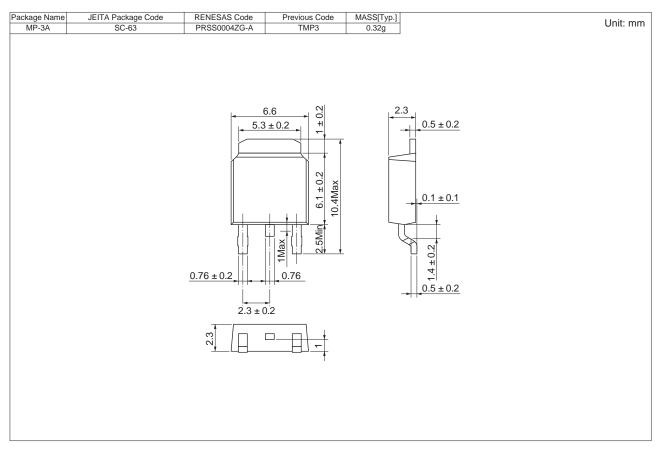








#### **Package Dimensions**



## **Ordering Information**

Orderable Part Number	Quantity	Shipping Container
RJK4532DPD-00#J2	3000 pcs	Taping



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